

FIG. 1

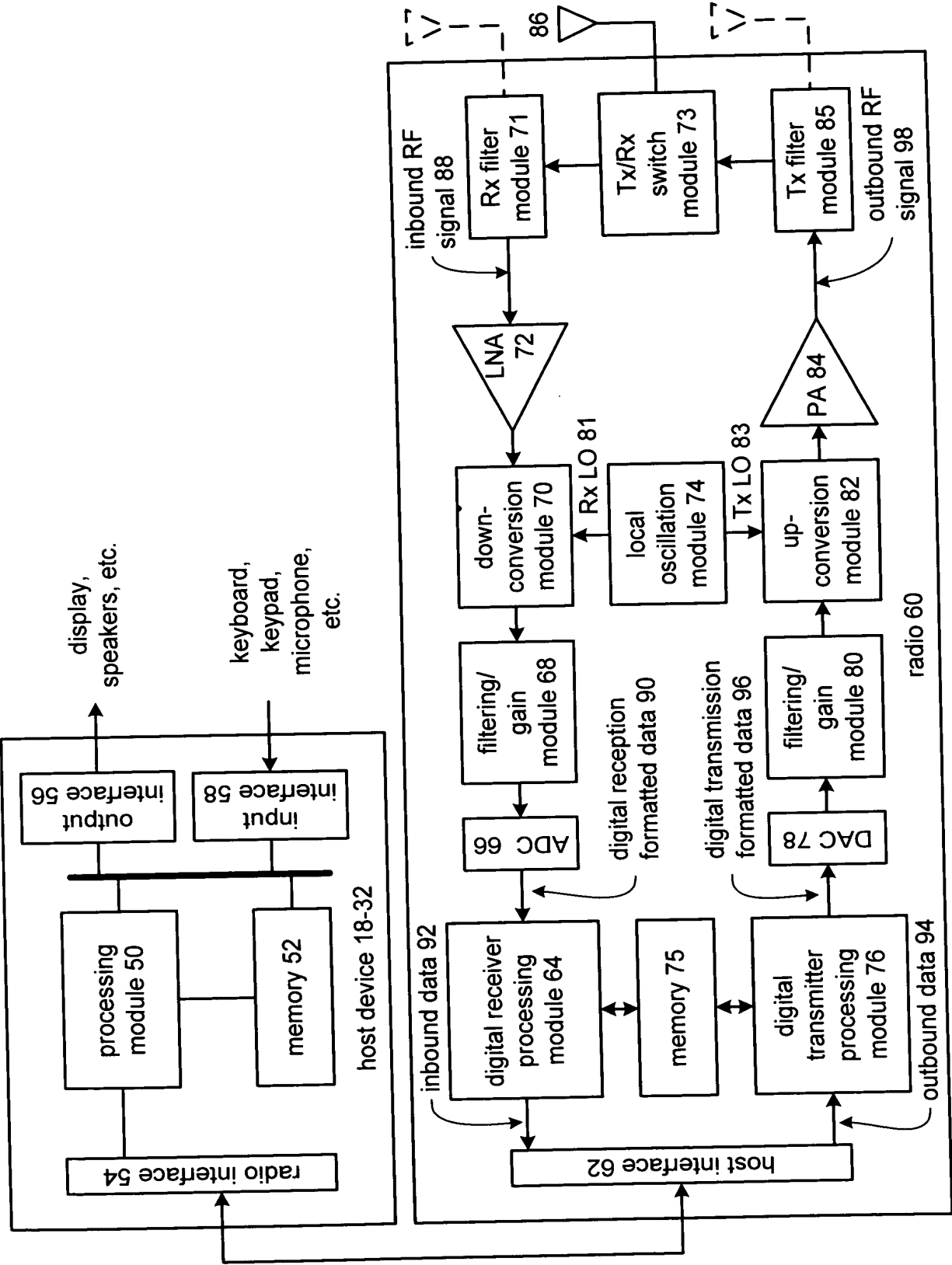


FIG. 2

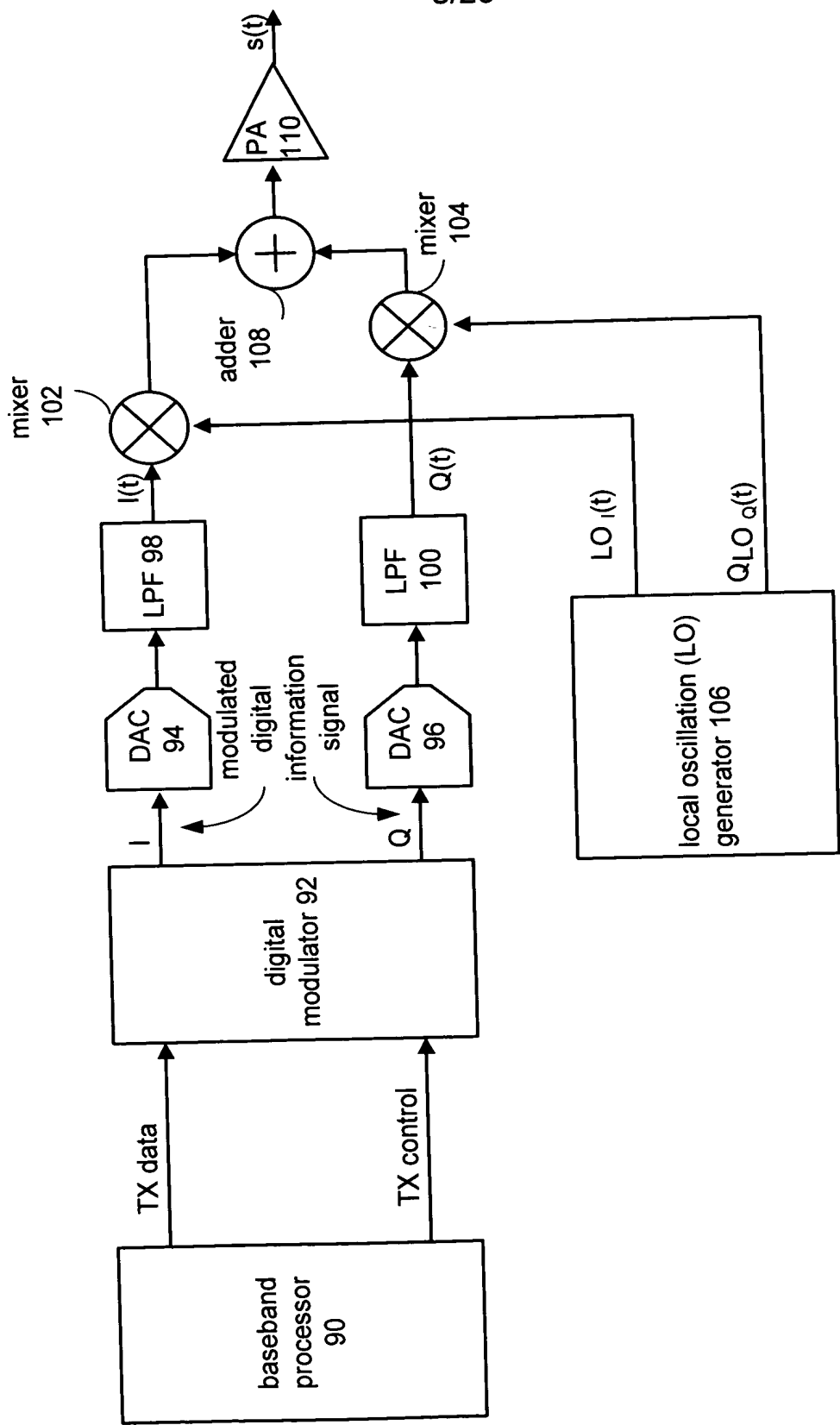


FIG. 3 radio transmitter 88

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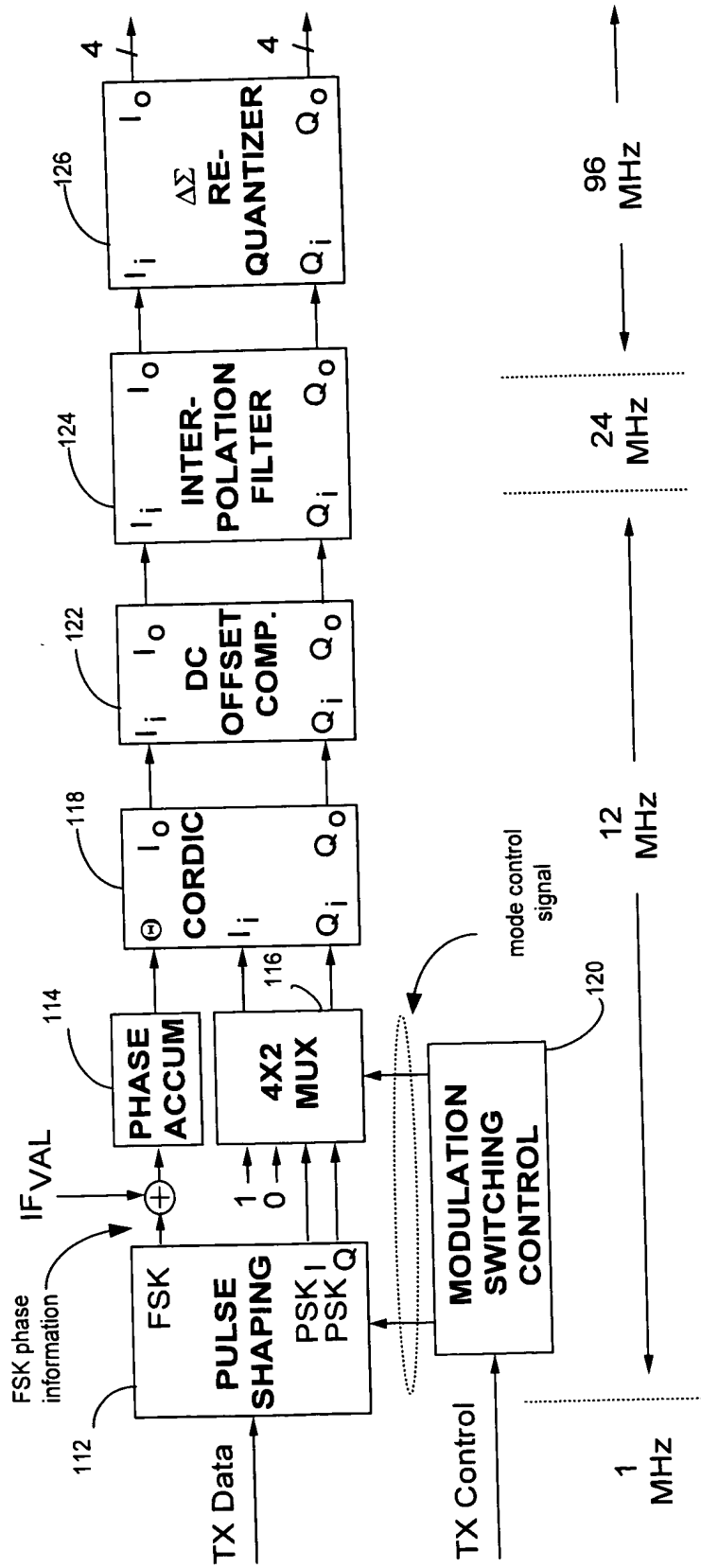


FIG. 4 digital modulator 92

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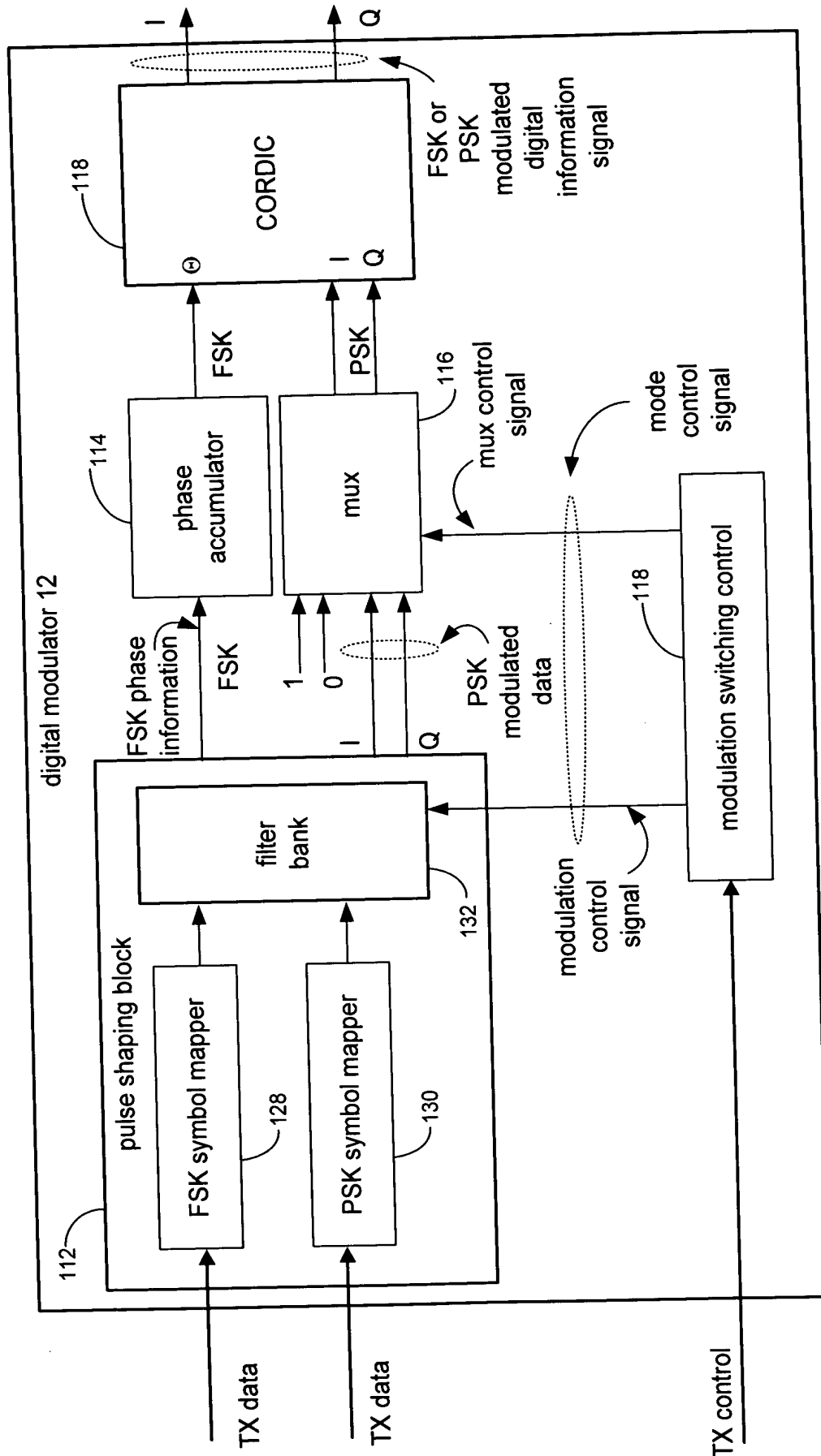
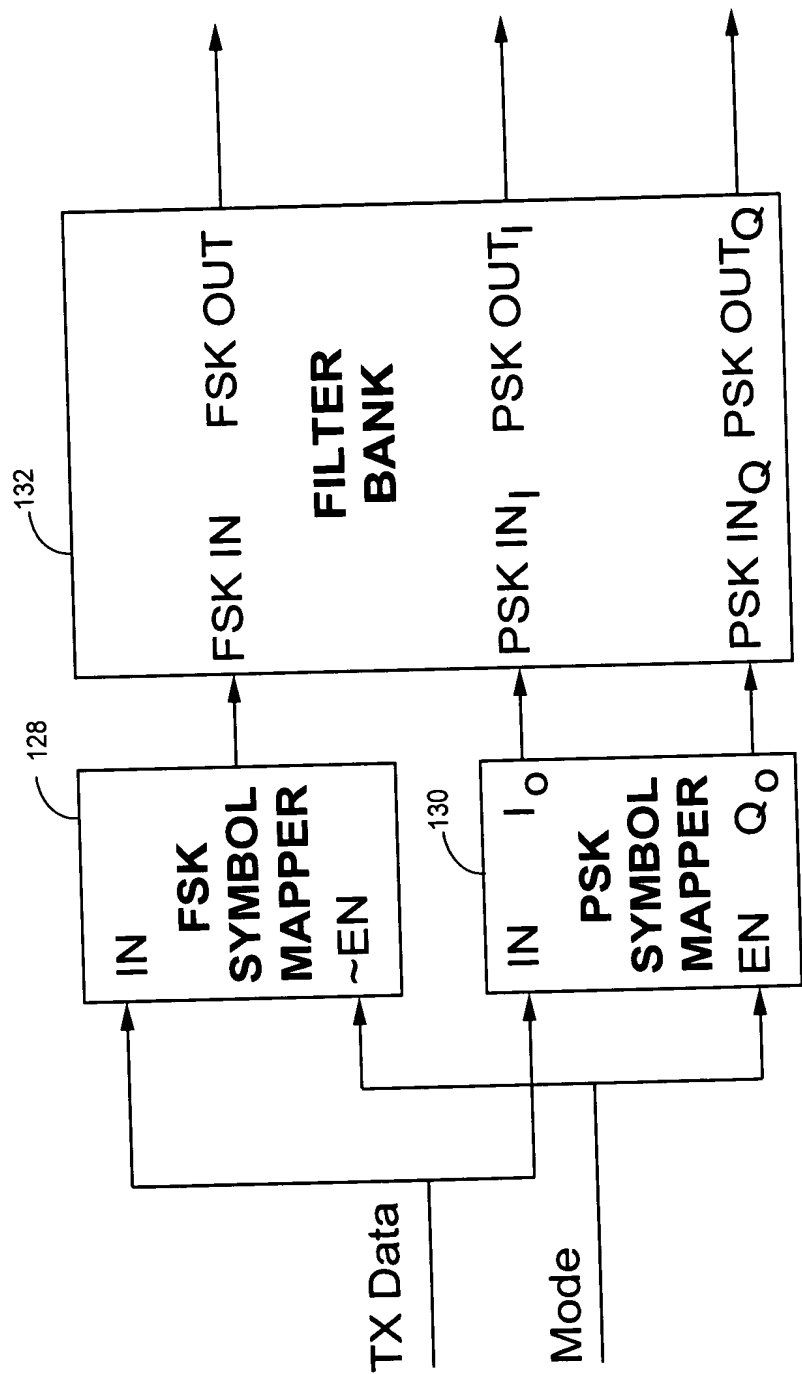


FIG. 5 digital modulator 92



pulse shaping
block

FIG. 6

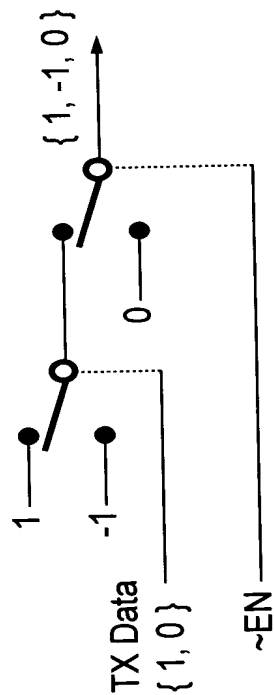


Figure 7 FSK symbol mapper

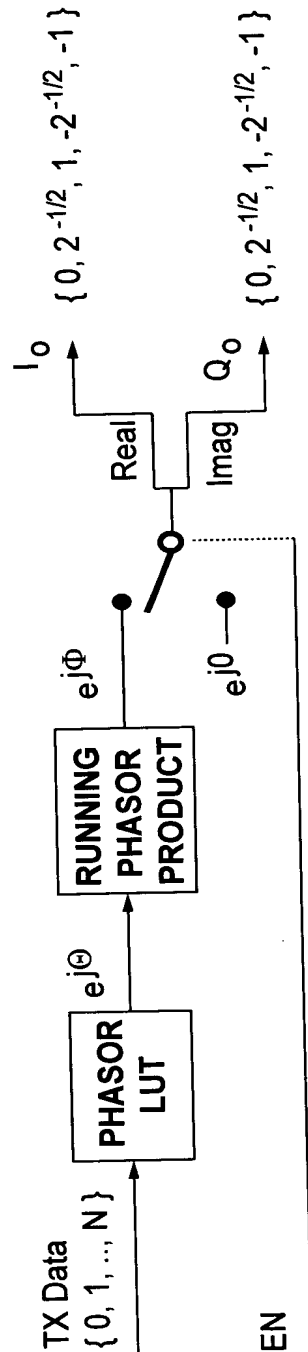


Figure 8 PSK symbol mapper

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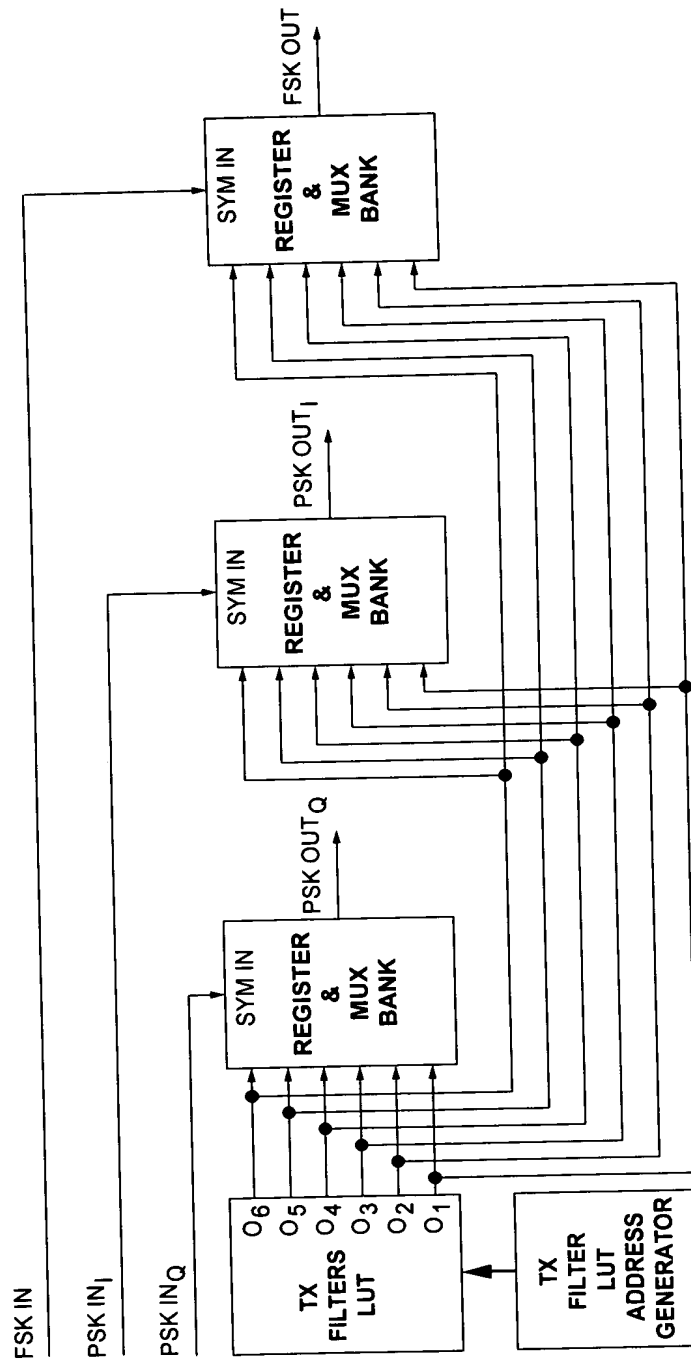


Figure 9

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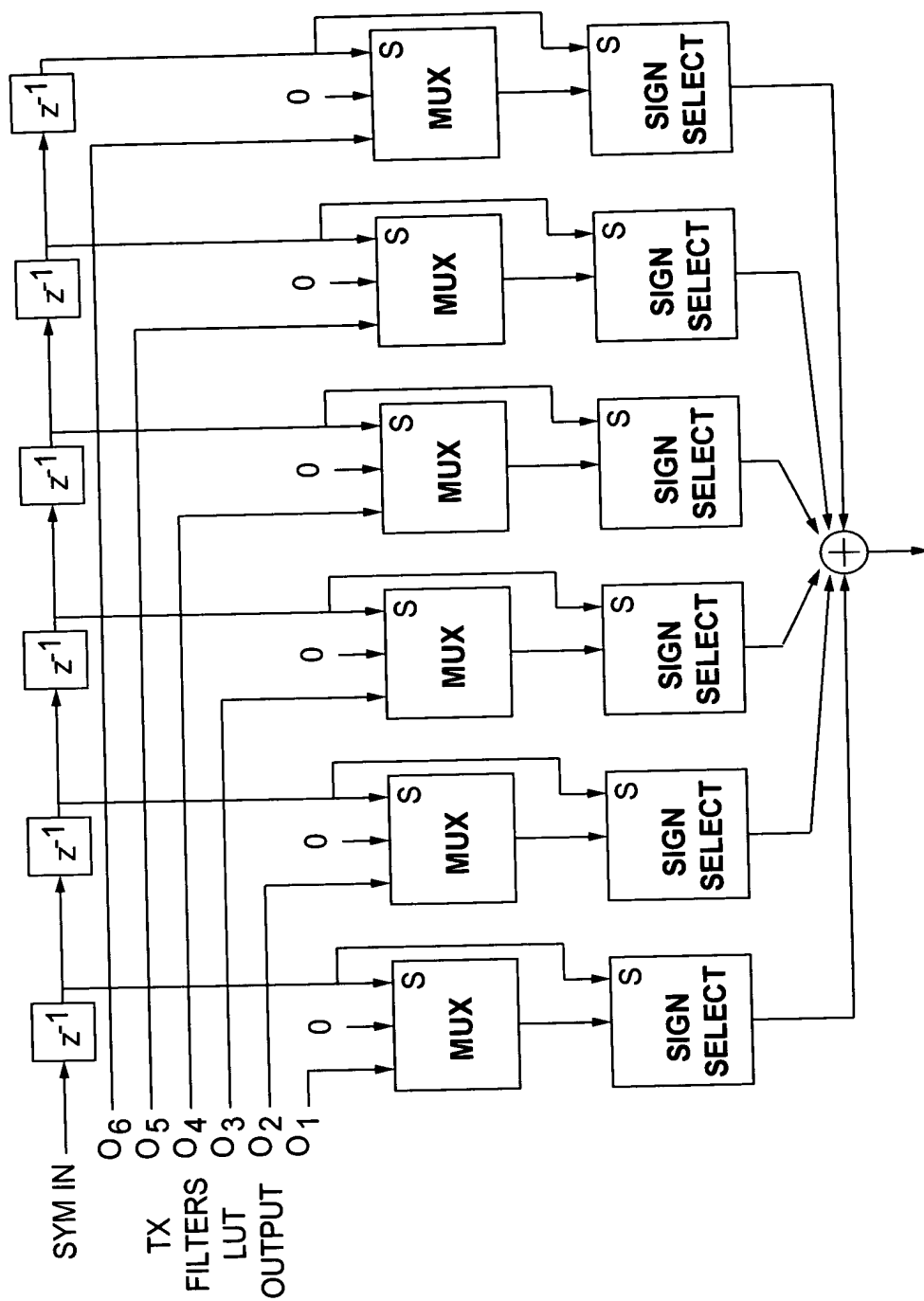


FIG. 10

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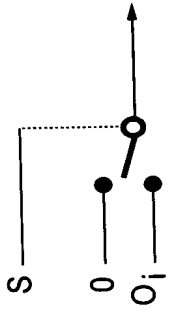


FIG. 11

FIG. 12

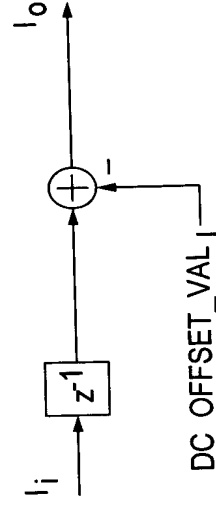


FIG. 16

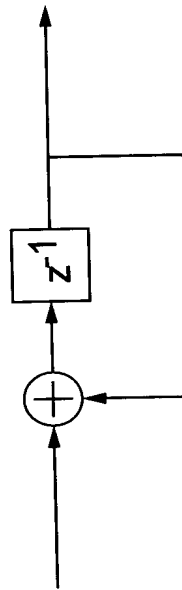


FIG. 13

DC_OFFSET_VAL_I

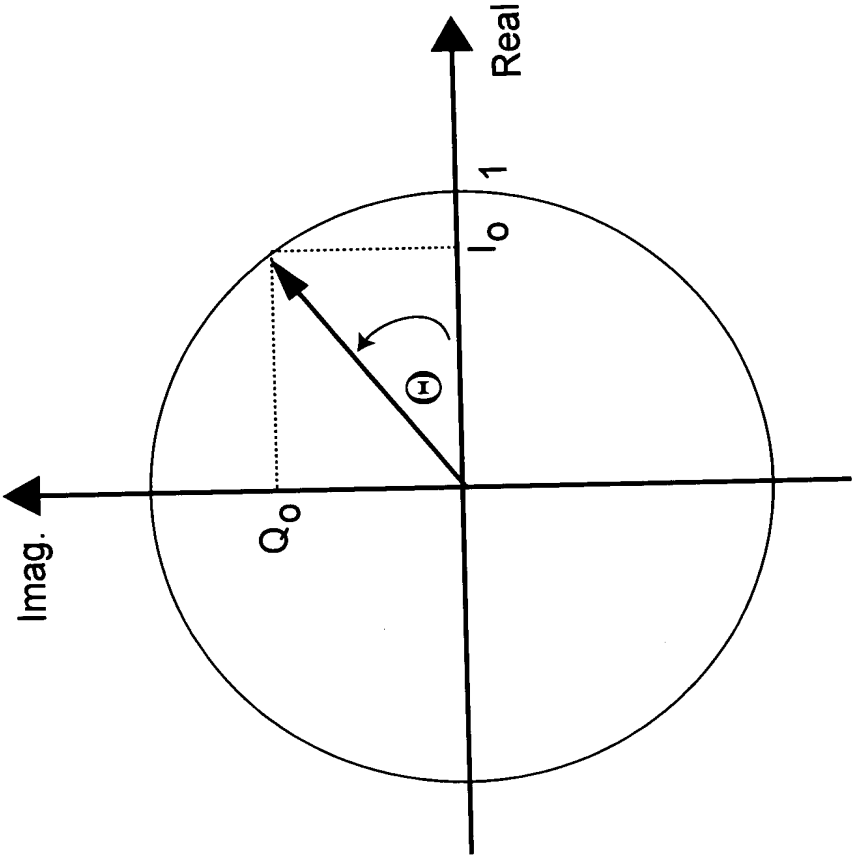


FIG. 14A

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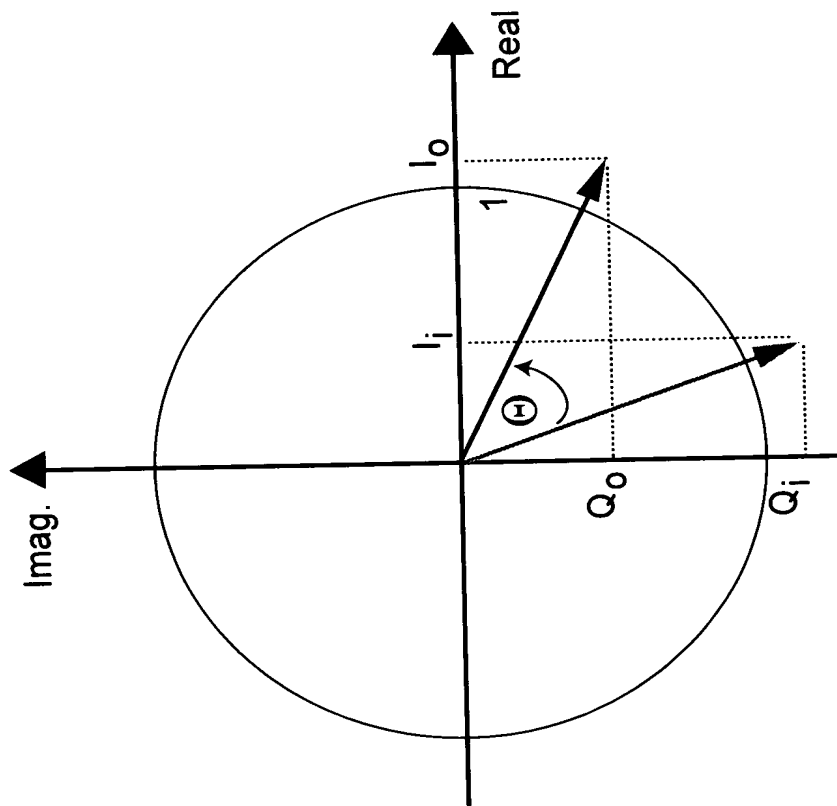


FIG. 14B

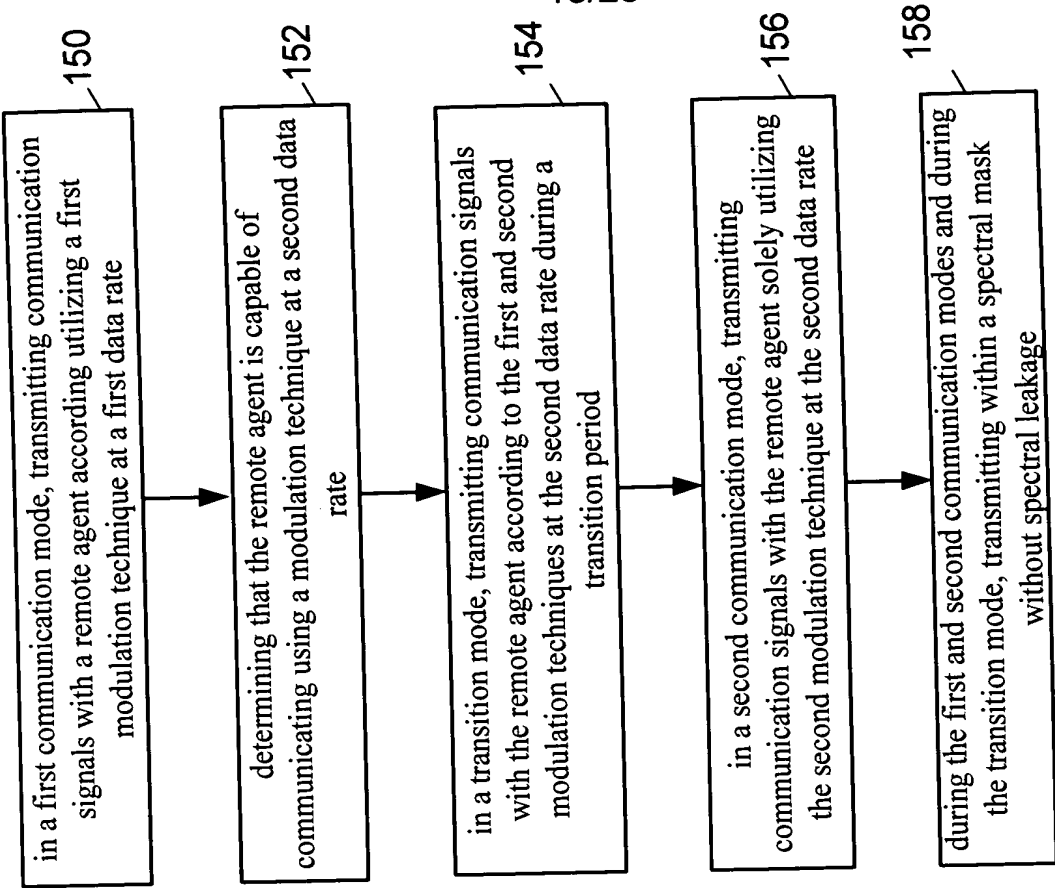


FIG 15B

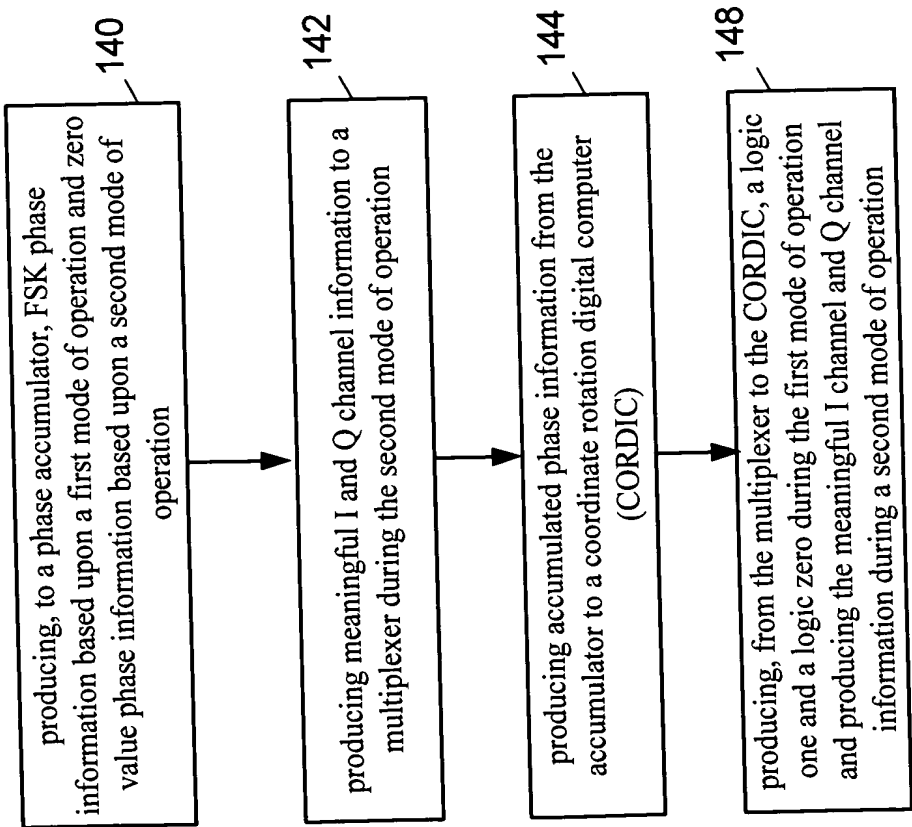


FIG 15A

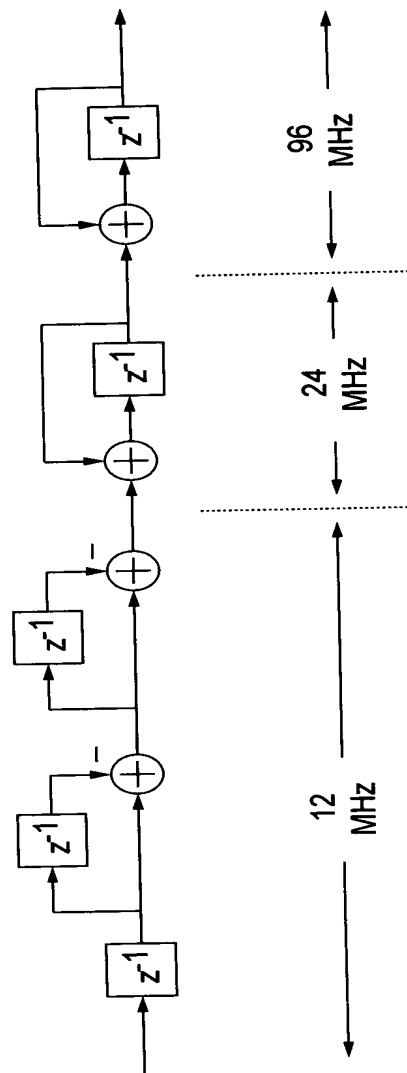


FIG 17

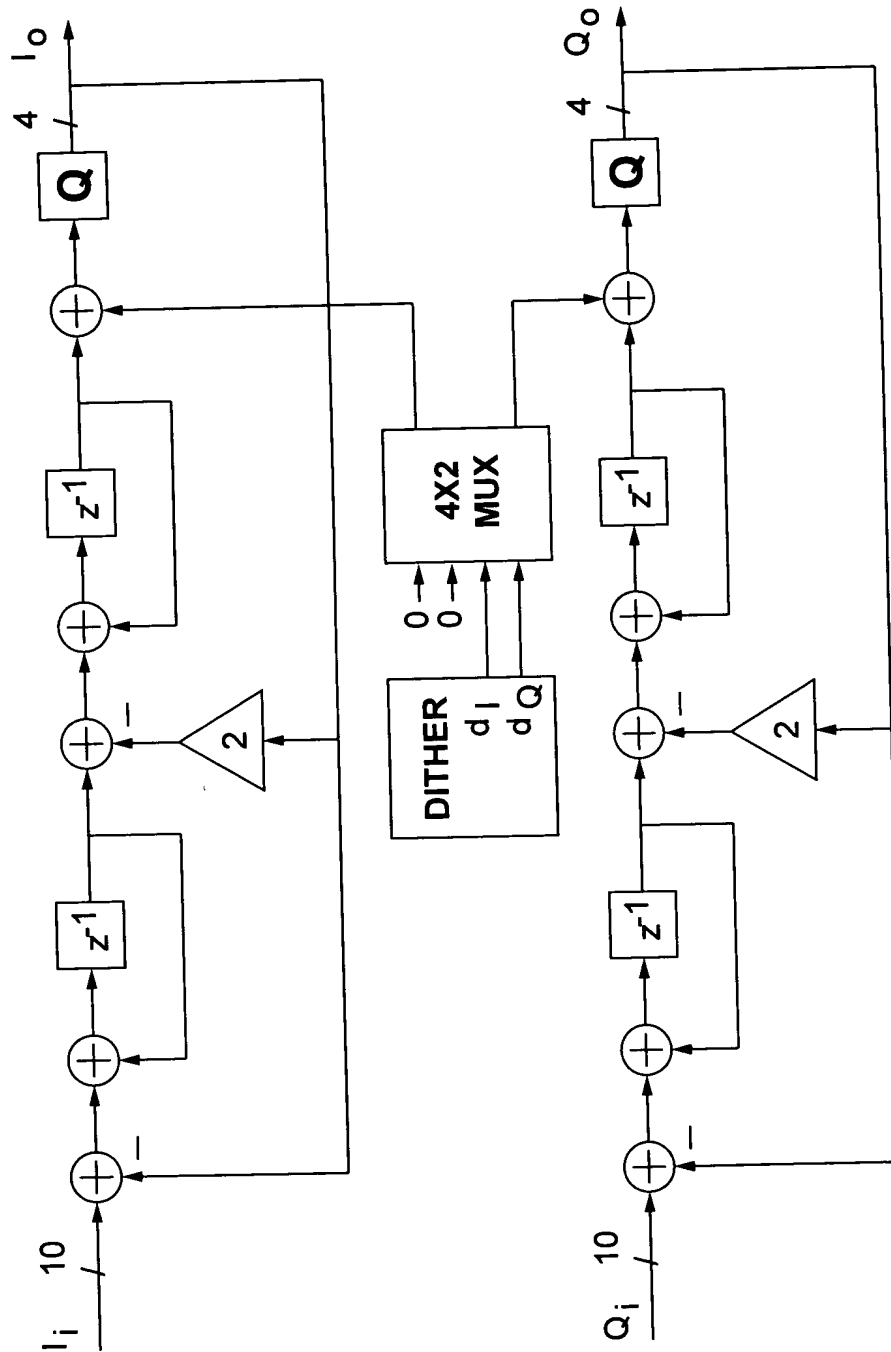


FIG 18

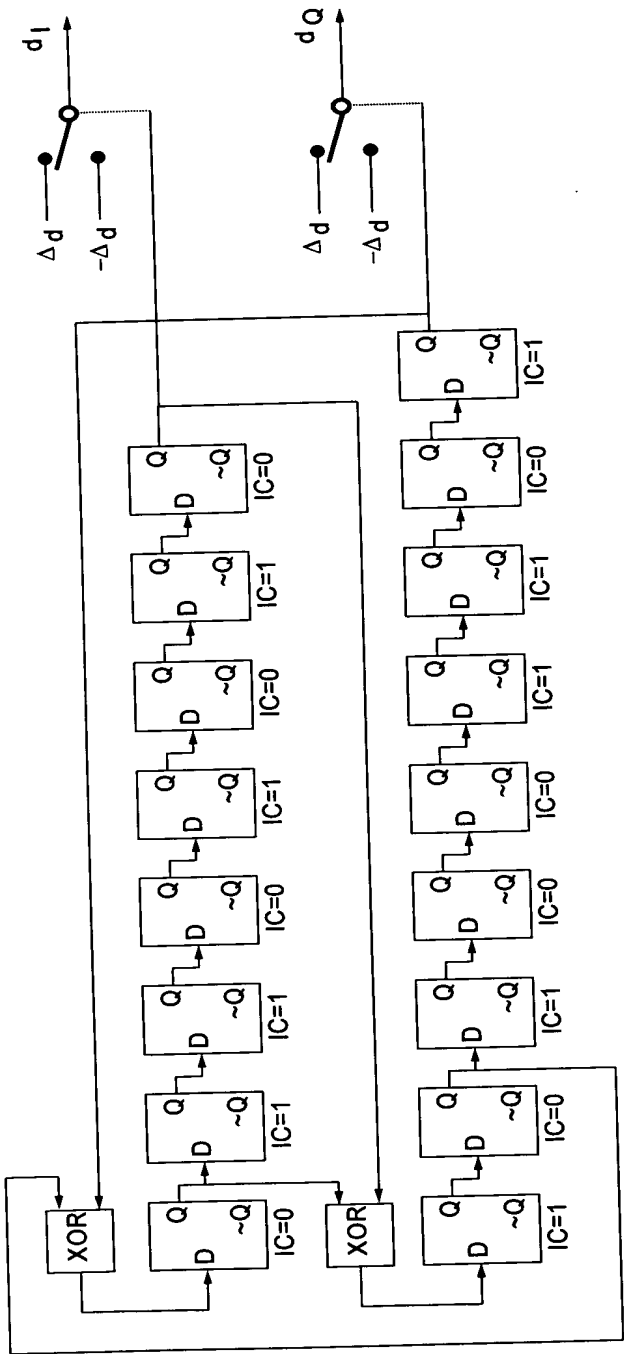


FIG 19

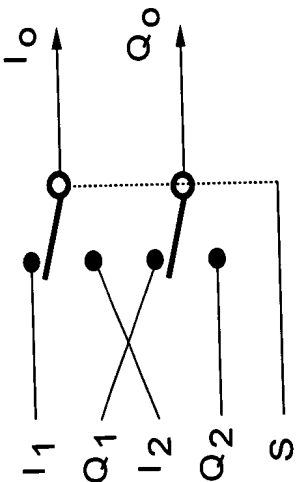


FIG 20

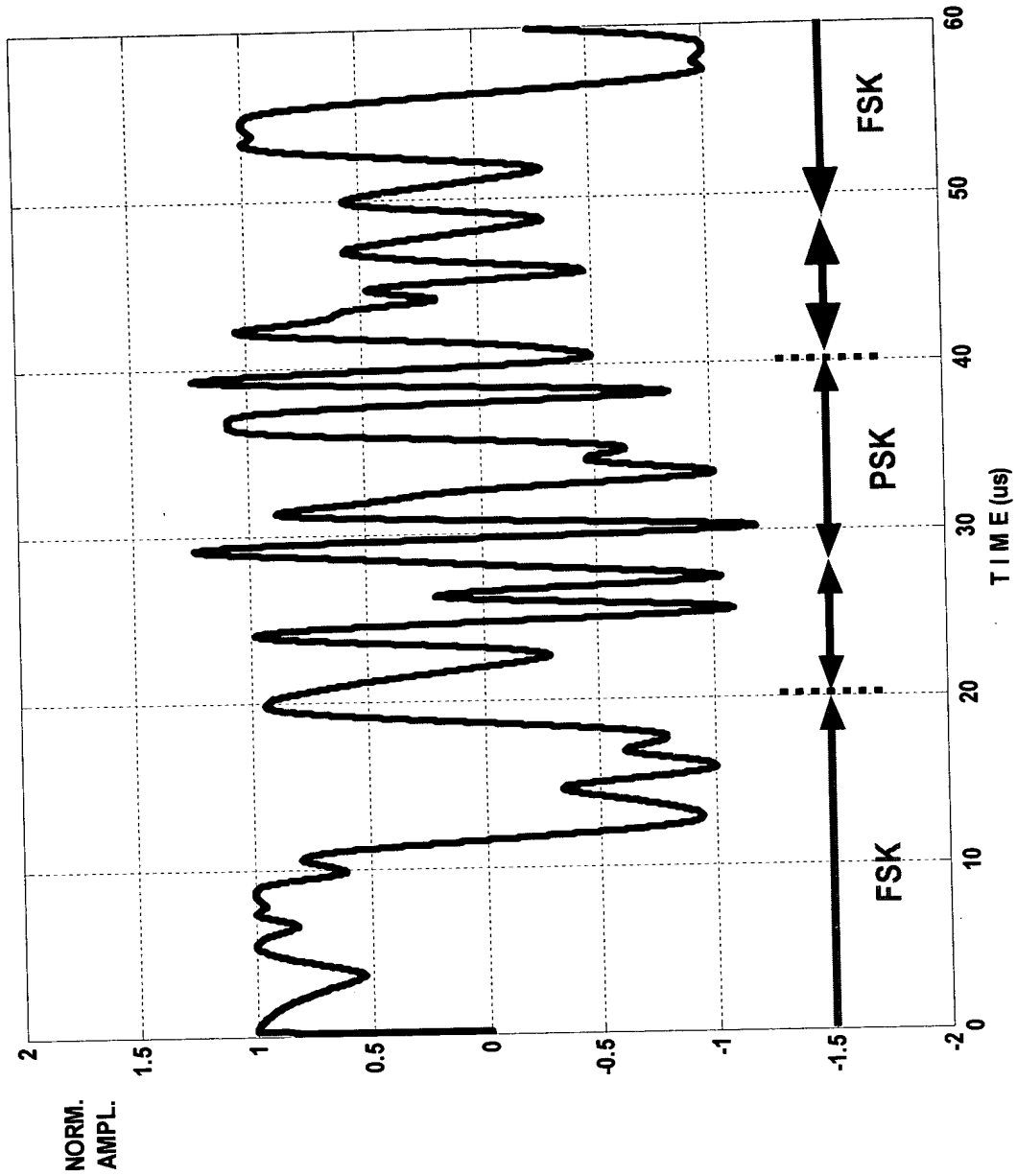


FIG 21

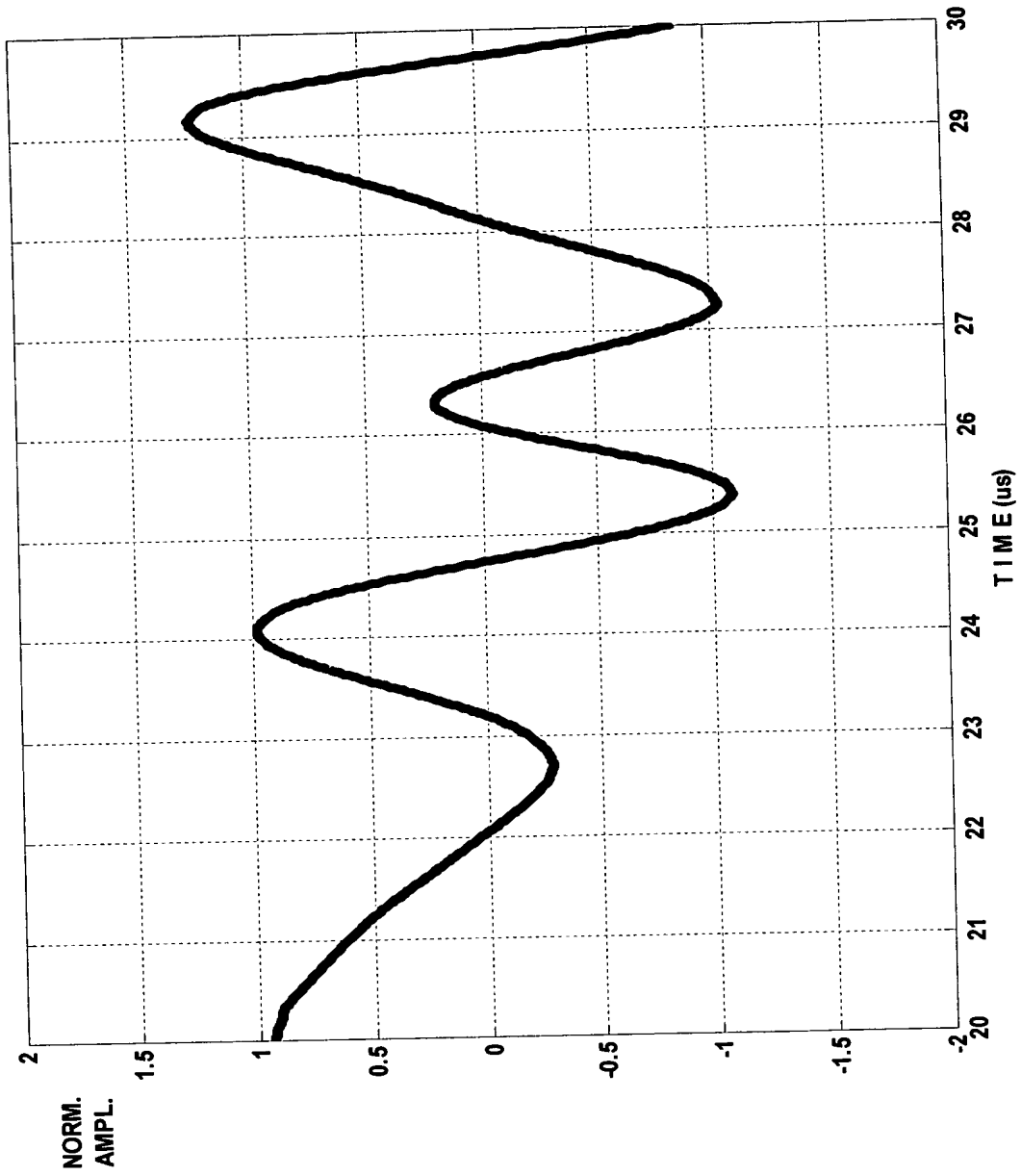


FIG 22

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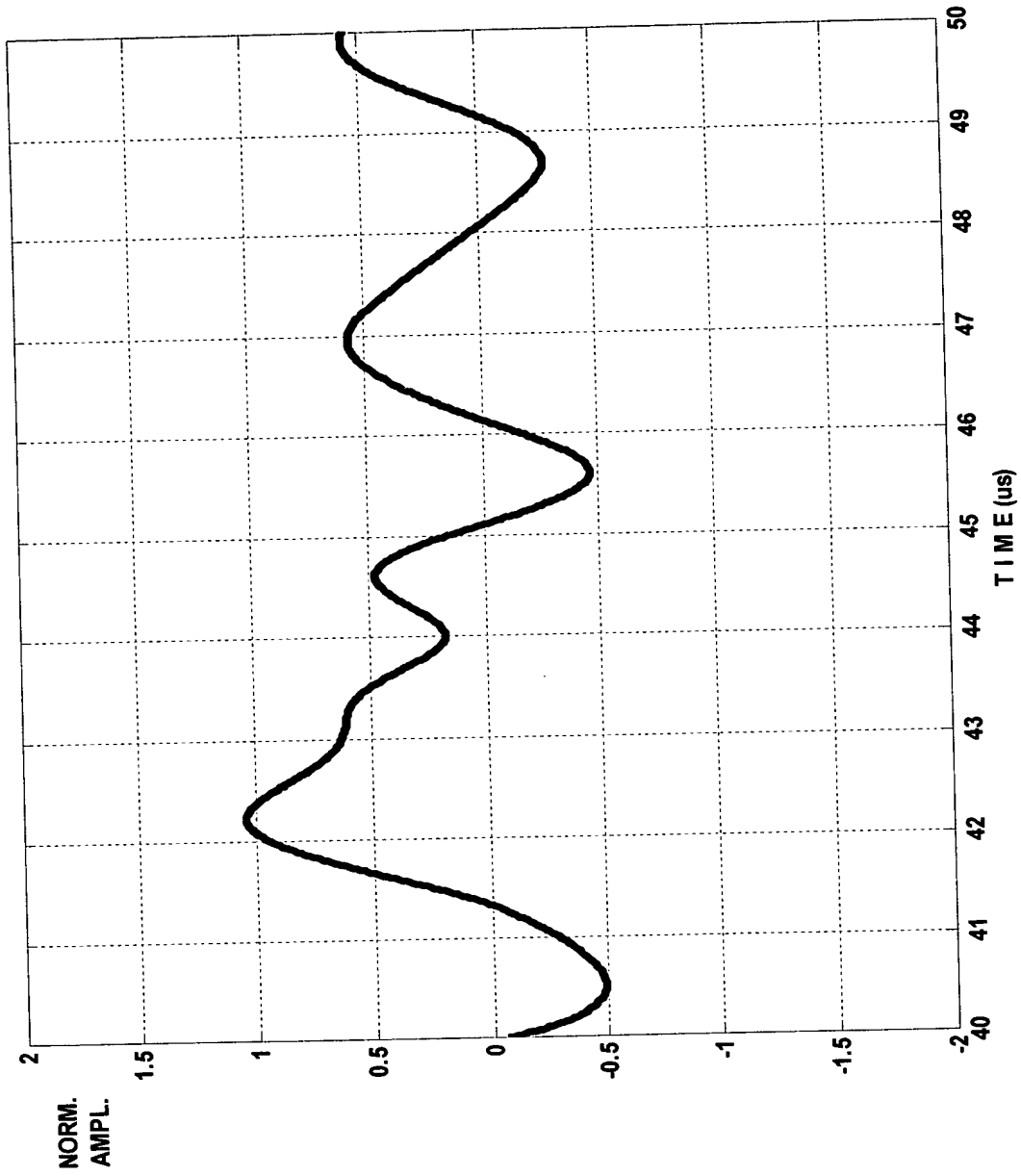


FIG 23

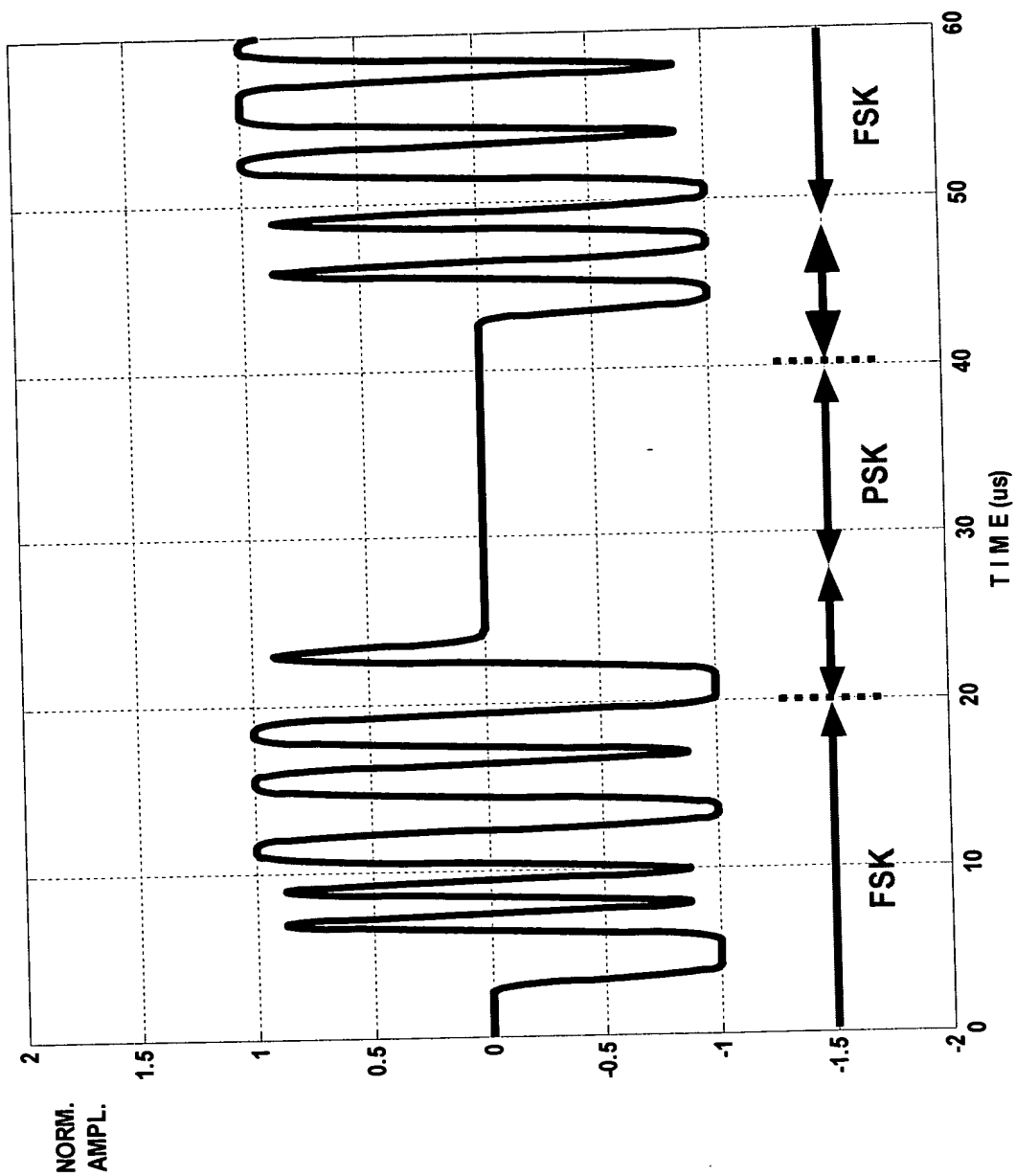


FIG 24

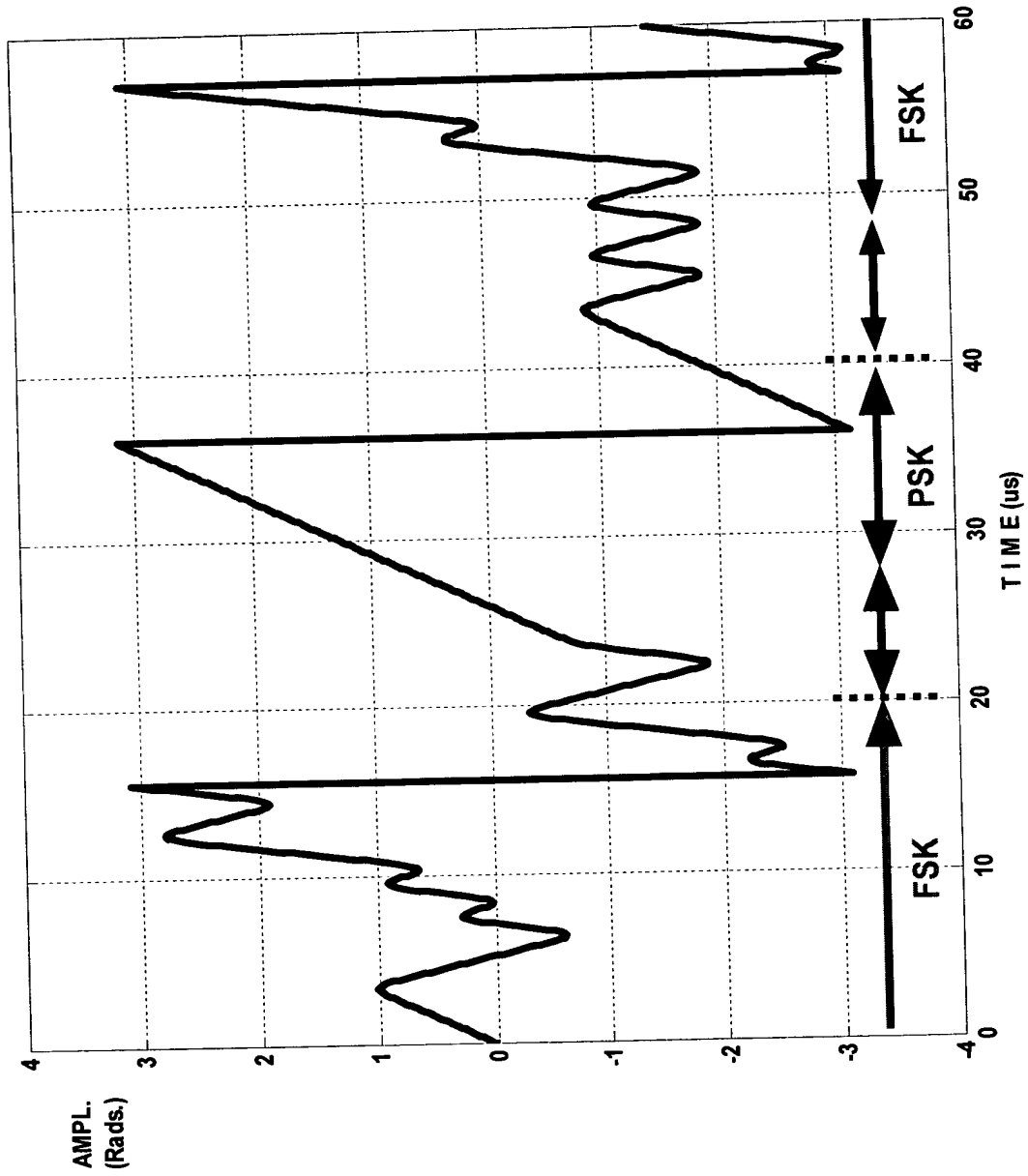


FIG 25 4x2 mux Q channel behavior

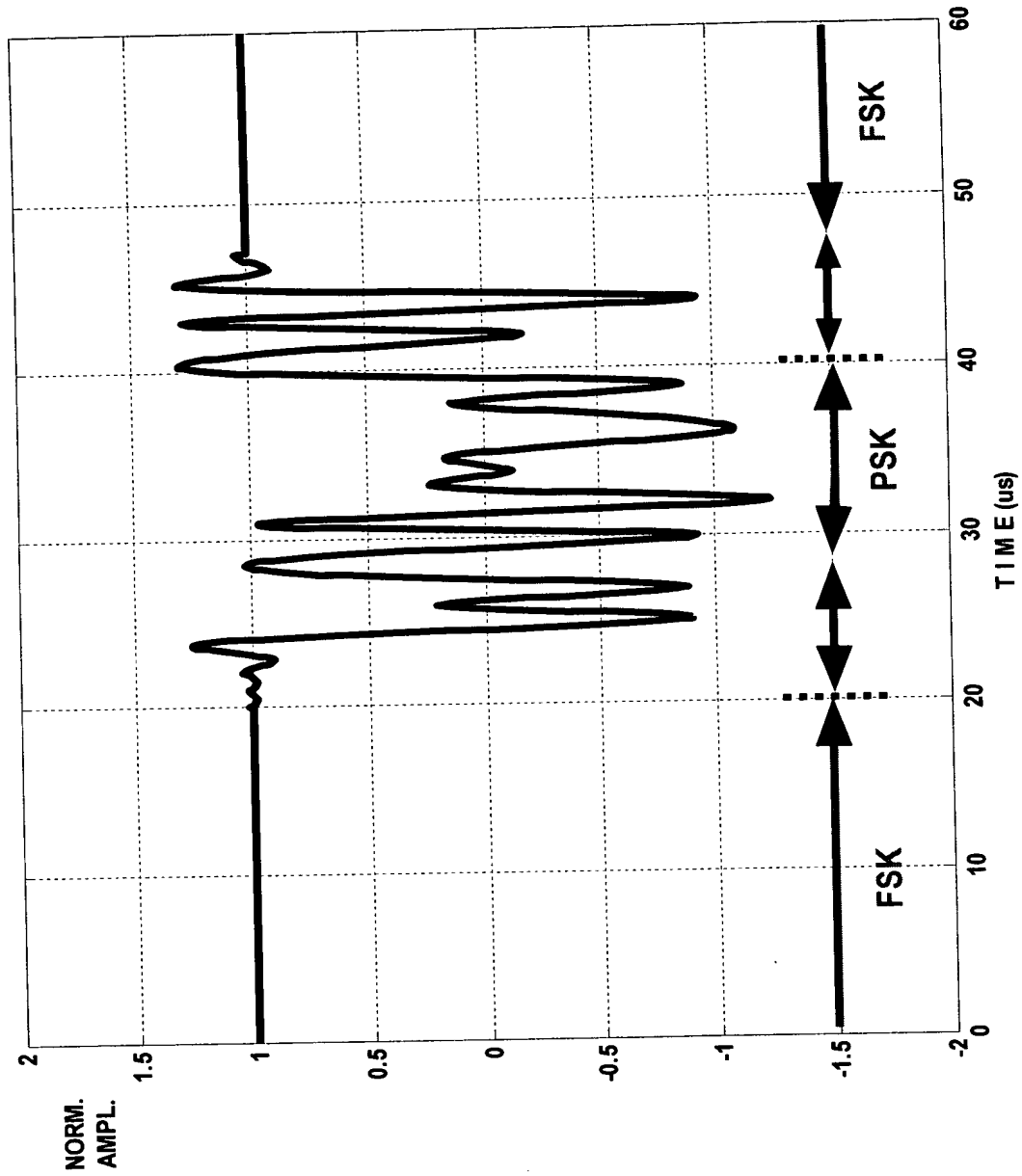


FIG 26 4x2 mux 1 channel behavior

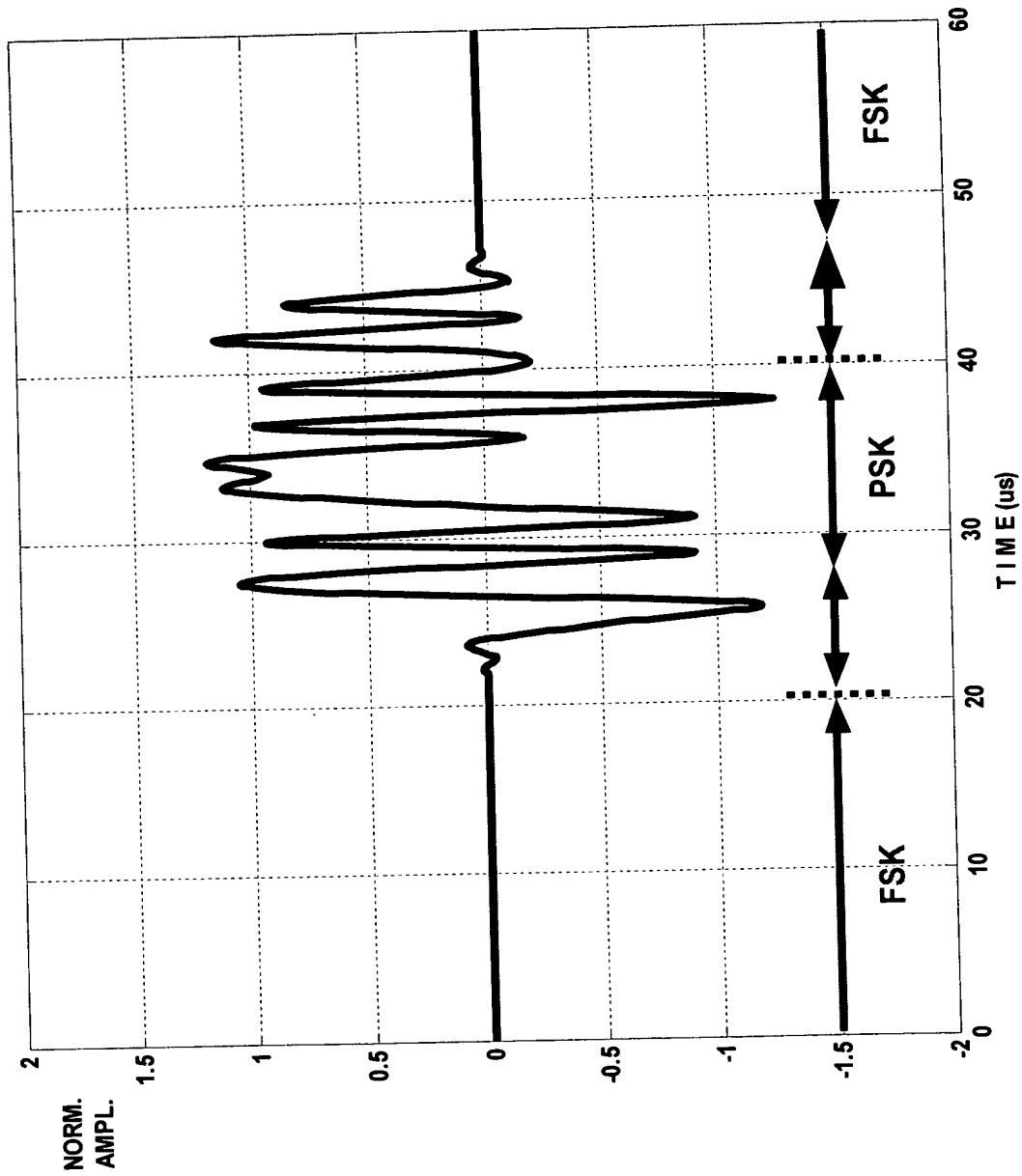


FIG 27 4x2 mux Q channel behavior

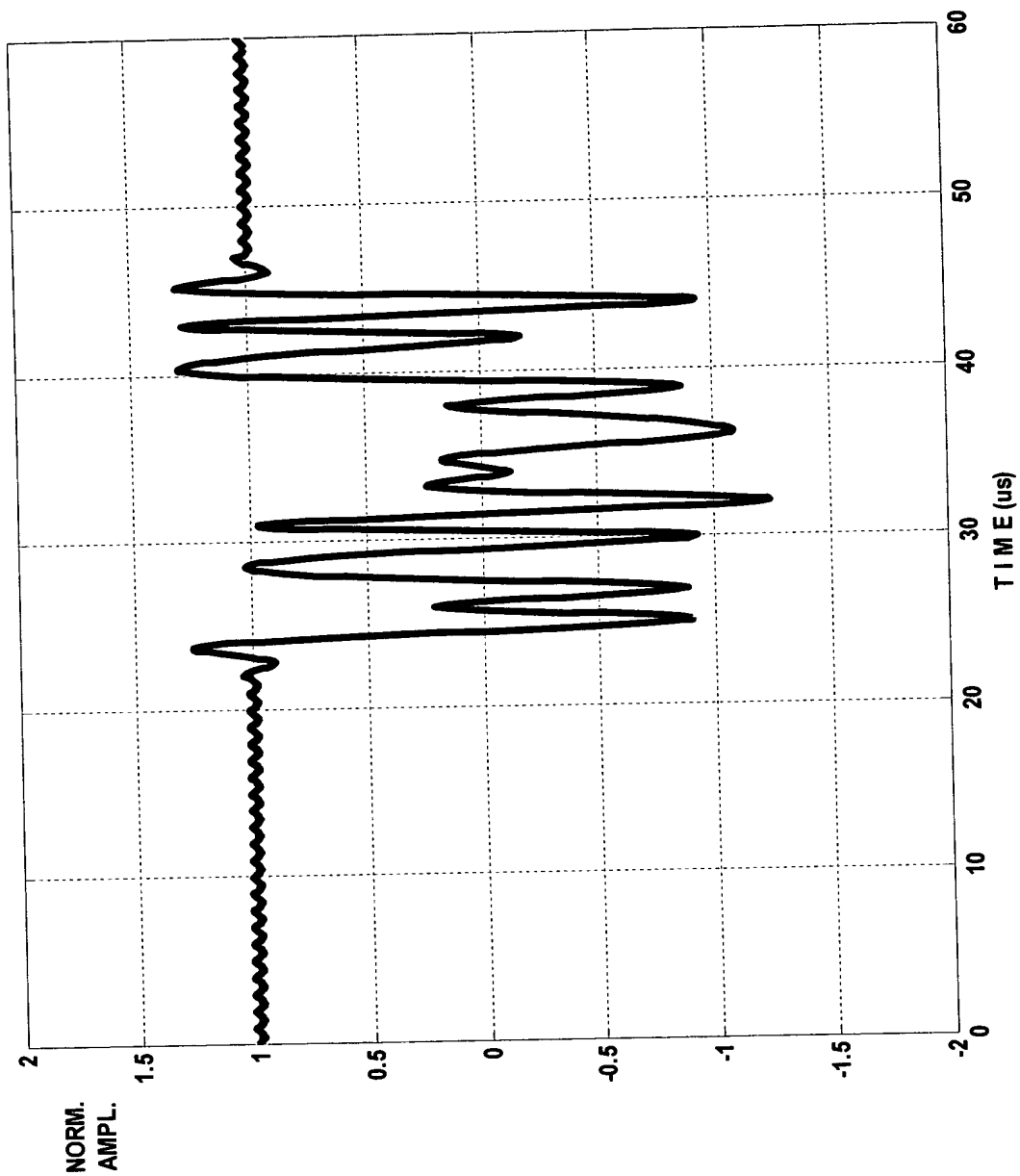


FIG 28 PSK_i behavior in transition

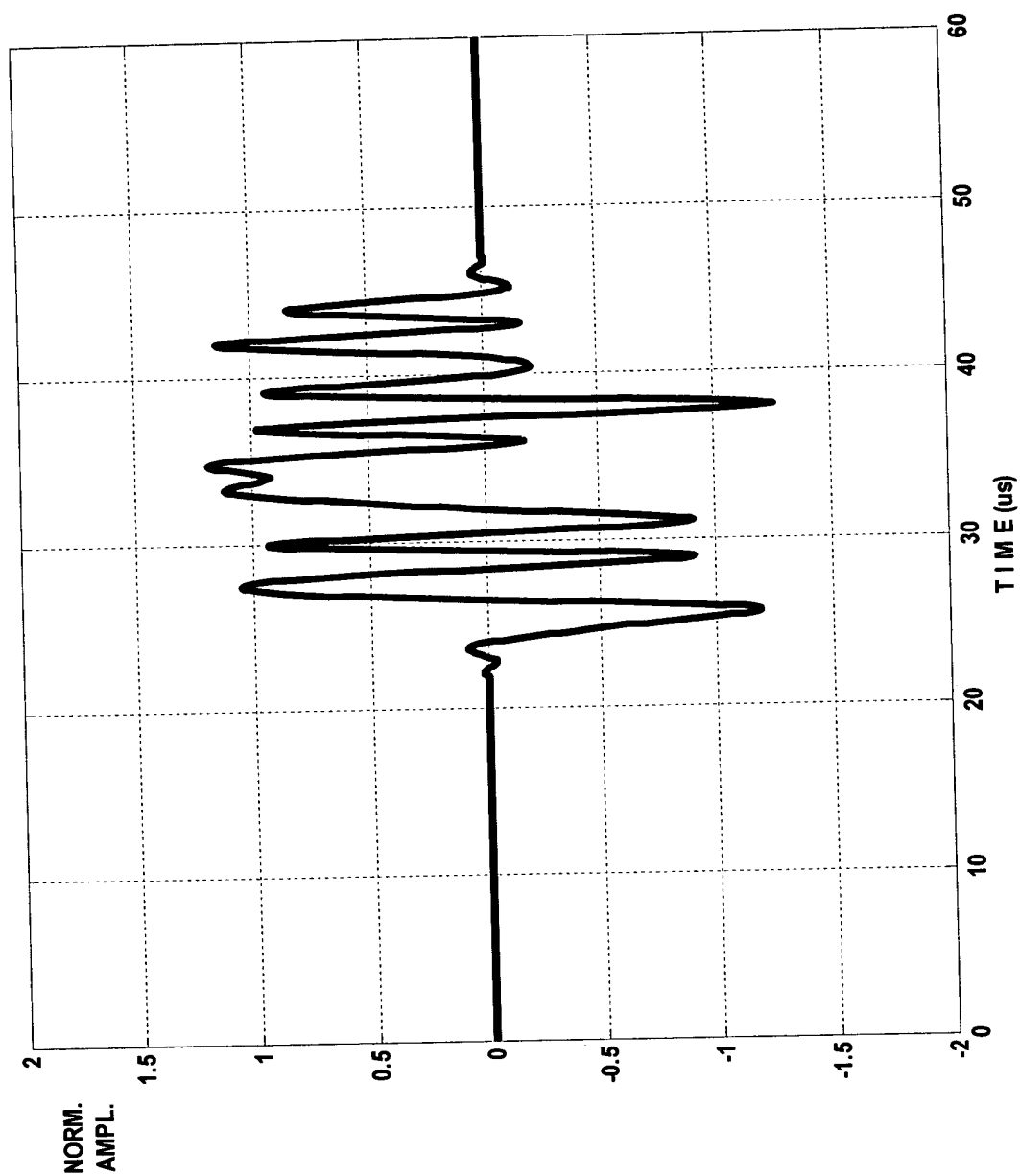


FIG 29 PSK_Q behavior in transition